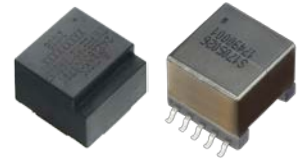


Wire Wound Transformer–TWP/TWS Series

Operating Temp. : -40℃~+125℃

Customized



PRODUCT IDENTIFICATION

T **W** **P** **EP** **07** **06** **09** **B** **3** **01** **T**
 ① ② ③ ④ ⑤ ⑥ ⑦ ⑧ ⑨ ⑩ ⑪

①

Type	
T	Transformer

②

Structure	
W	Wire

③

Feature Code	
S	for Signal
P	for Power
N	for Network

④

Core Type	
EE/EP etc.	

⑥

Pin Bobbin Size of Pin Direct Typ. (Refer to Mark D in Fig.2)	
06	6.0mm
18	18mm

⑧

Style	
A	&Horizontal & Pin
B	&SMDHorizontal & SMD
C	&Vertical & Pin
D	&SMDVertical & SMD

⑤

Core Size Typ. (Refer to Mark A in Fig.1)	
07	7.0mm
16	16mm

⑦

Product Height Typ. (Refer to Mark H in Fig.3)	
09	9.0mm

⑨

Number of Coil	
1~9	

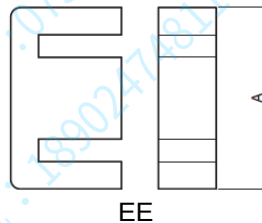
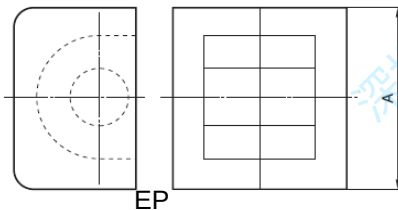
⑩

Design Code	
00~99, A0~Y9	

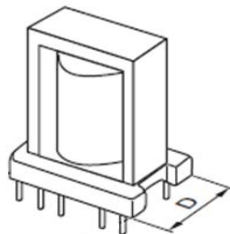
⑪

Packing	
T--Taping, P--Pallet, B--Bulk	

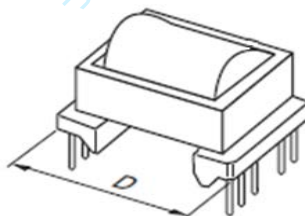
Core Type & Core A Size Fig. 1



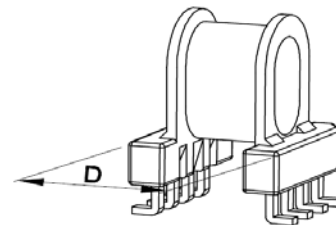
Bobbin Size of Pin Direct Fig. 2



Vertical

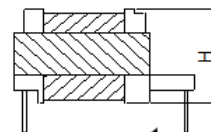
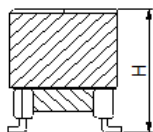


Horizontal



Horizontal

Product Height Fig. 3



Stand off

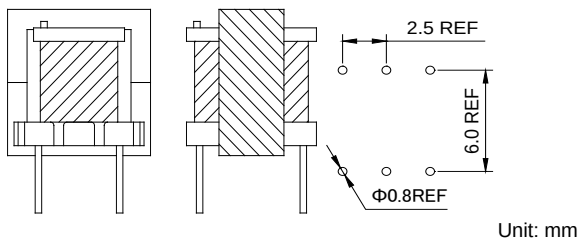
Core Type EE Reference Data — Power Transformer

FEATURES AND APPLICATIONS

- Common types, open shape for easy selection and good heat dissipation
- Widely used in the power of program-controlled switches, high-power UPS inverter power supplies, computer power, energy-saving lamps, and so on

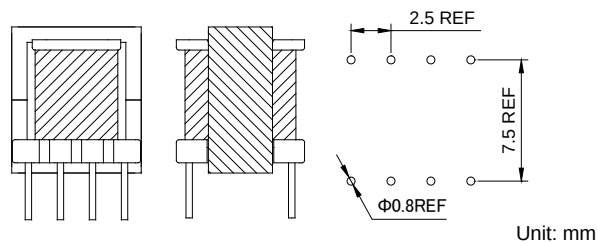
SPECIFICATION

■ P/N: TWPEE080809C□□□P



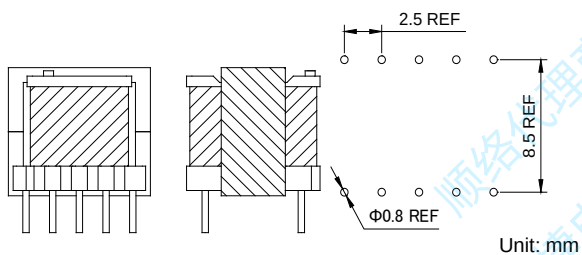
Core Type: EE08 Recommended PCB Layout
Reference dimension (L×W×H): 10×10×10mm Max.
Power: 2.5W Max.

■ P/N: TWPEE101011C□□□P



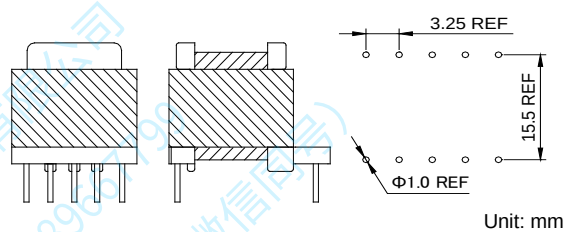
Core Type: EE10 Recommended PCB Layout
Reference dimension (L×W×H): 12×12×12mm Max.
Power: 5W Max.

■ P/N: TWPEE131213C□□□P



Core Type: EE13 Recommended PCB Layout
Reference dimension (L×W×H): 14×14×14mm Max.
Power: 7W Max.

■ P/N: TWPEE161813A□□□P



Core Type: EE16 Recommended PCB Layout
Reference dimension (L×W×H): 20×17×14mm Max.
Power: 10W Max.

Core Type EP Reference Data — Signal Transformer

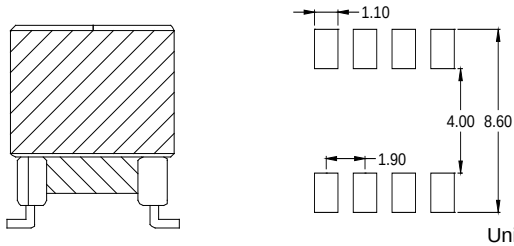
FEATURES AND APPLICATIONS

- Excellent magnetic shielding, low distributed capacitance
- Excellent THD, LB, insertion loss, return loss performance
- Most of them are suitable for SMD structure
- Applied to network equipment, hubs, IPTV, LAN and so on that adopt xDSL and G. Fast Technology

SPECIFICATION

■ P/N: TWPEP070609B□□□T

■ P/N: TWSEP070609B□□□T



Core Type: EP05

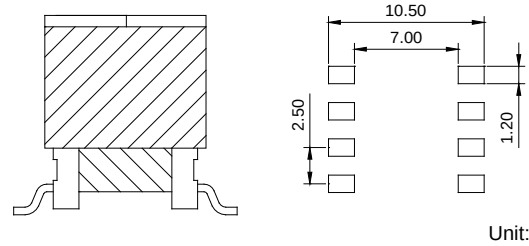
Recommended PCB Layout

Reference dimension (L×W×H): 8.0×7.0×10mm Max.

Power: 3W Max.

■ P/N: TWPEP090710B□□□T

■ P/N: TWSEP090710B□□□T



Core Type: EP07

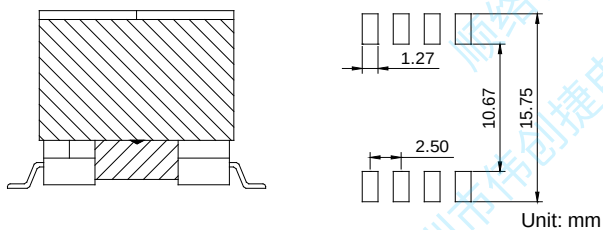
Recommended PCB Layout

Reference dimension (L×W×H): 10×10×11mm Max.

Power: 5W Max.

■ P/N: TWPEP121110B□□□T

■ P/N: TWSEP121110B□□□T



Core Type: EP10

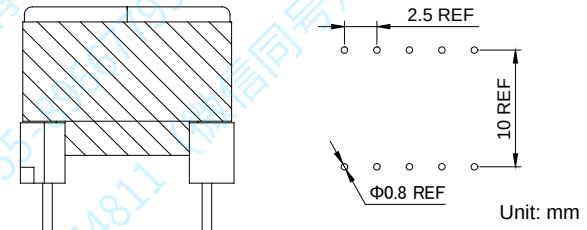
Recommended PCB Layout

Reference dimension (L×W×H): 16×13×11mm Max.

Power: 10W Max.

■ P/N: TWPEP131412A□□□P

■ P/N: TWSEP131412A□□□P



Core Type: EP13

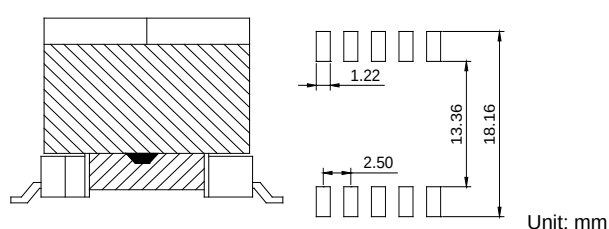
Recommended PCB Layout

Reference dimension (L×W×H): 15×14×13mm Max.

Power: 15W Max.

■ P/N: TWPEP131312B□□□T

■ P/N: TWSEP131312B□□□T



Core Type: EP13

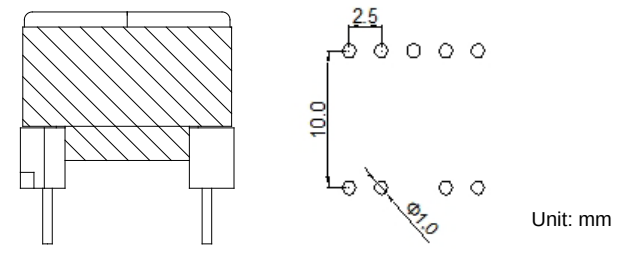
Recommended PCB Layout

Reference dimension (L×W×H): 18×14×13mm Max.

Power: 15W Max.

■ P/N: TWPEP141514A□□□F

■ P/N: TWSEP141514A□□□F



Core Type: EP14

Recommended PCB Layout

Reference dimension (L×W×H): 14.5×14.5×13.5mm Max.

Power: 15W Max.

Core Type EP/EFD for PoE Power Reference Data — Power Transformer

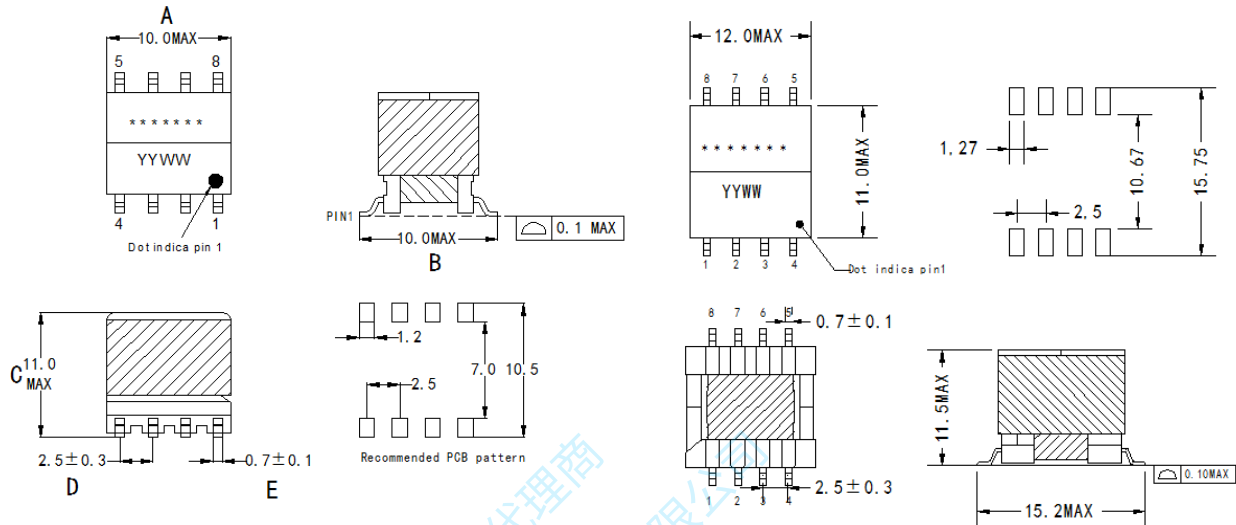
FEATURES AND APPLICATIONS

- Excellent magnetic shielding, low distributed capacitance
- Low loss, low leakage inductance, uniform magnetic field
- Suitable for PoE power supply

SHAPE AND DIMENSIONS

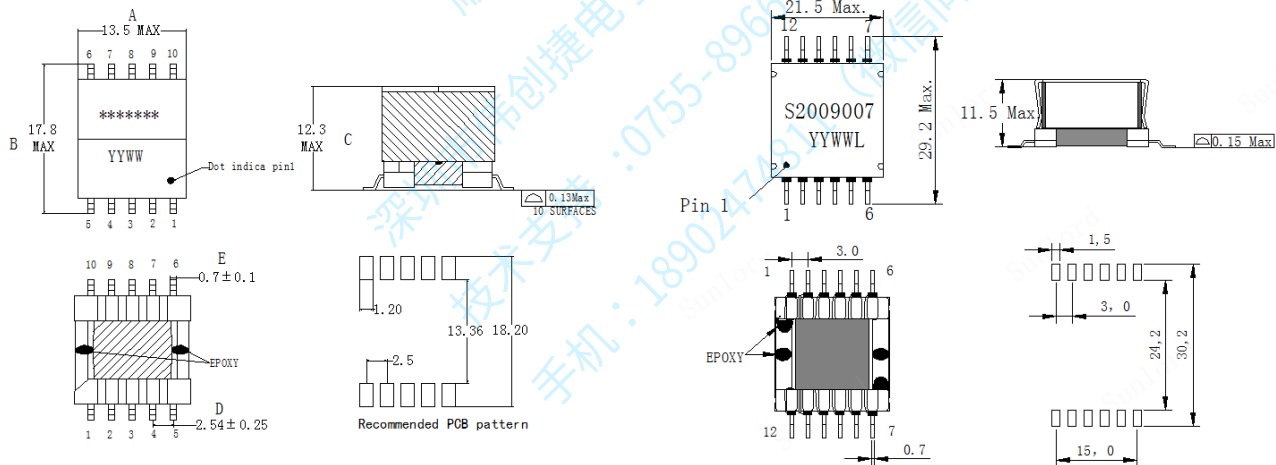
■ P/N: TWPEP090711B□□□T

■ P/N: TWPEP121112B□□□T



■ P/N: TWPEP131313B□□□T

■ P/N: TWPEE203113B403T



SPECIFICATION

Part No.	Test Item	Test Terminal	Test Condition	Specification	Schematic
TWPEP090711B308T Marking:(S1612015)	L	Pin(3-4)	250KHz,0.1V	310 μ H $\pm$ 10%	Suggest Equivalent Circuit
	LK	Pin(3-4) (short 1+2,5~8)	250KHz,0.1V	4.25 μ H Max.	
	DCR	Pin(3-4)	25 $\pm$ 5 $^{\circ}$ C	1040m Ω Max.	
		Pin(5,6-7,8)		700m Ω Max.	
		Pin(2-1)		2010m Ω Max.	
	Isat	Pin(3-4)	250KHz,0.1V @L $\geq$ 90%	0.3A Min.	
	Turns Raito	Pri: Sec: Bias	15.75KHz,1V	1:0.7:0.7 $\pm$ 3%	
	Hi-Pot	Pri&Bias to Sec	50Hz, 3.0mA 1S	1500V	
		Winding to Core	50Hz, 3.0mA 1S	500V	
TWPEP090711B309T Marking:(S1612016)	L	Pin(3-4)	250KHz,0.1V	310 μ H $\pm$ 10%	Suggest Equivalent Circuit
	LK	Pin(3-4) (short 1+2,5~8)	250KHz,0.1V	4.75 μ H Max.	
	DCR	Pin(3-4)	25 $\pm$ 5 $^{\circ}$ C	1020m Ω Max.	
		Pin(5,6-7,8)		66m Ω Max.	
		Pin(2-1)		2010m Ω Max.	
	Isat	Pin(3-4)	250KHz, 0.1V @L $\geq$ 90%	0.3A Min	
	Turns Raito	Pri:Sec:Bias	15.75KHz,1V	1:0.19:0.7 $\pm$ 3%	
	Hi-Pot	Pri&Bias to Sec	50Hz, 3.0mA 1S	1500V	
		Winding to Core	50Hz, 3.0mA 1S	500V	
TWPEP090711B310T Marking:(S1612017)	L	Pin(3-4)	250KHz,0.1V	310 μ H $\pm$ 10%	Suggest Equivalent Circuit
	LK	Pin(3-4) (short 1+2,5~8)	250KHz,0.1V	4.50 μ H Max.	
	DCR	Pin(3-4)	25 $\pm$ 5 $^{\circ}$ C	1020m Ω Max.	
		Pin(5,6-7,8)		118m Ω Max.	
		Pin(2-1)		2010m Ω Max.	
	Isat	Pin(3-4)	250KHz,0.1V @L $\geq$ 90%	0.3A Min.	
	Turns Raito	Pri: Sec: Bias	15.75KHz,1V	1:0.28:0.7 $\pm$ 3%	
	Hi-Pot	Pri&Bias to Sec	50Hz, 3.0mA 1S	1500V	
		Winding to Core	50Hz, 3.0mA 1S	500V	

SPECIFICATION

Part No.	Test Item	Test Terminal	Test Condition	Specification	Schematic
TWPEP090711B312T Marking:(S1611005)	L	Pin(4-3)	320KHz,0.1V	310 μ H $\pm$ 10%	
	LK	Pin(4-3) (short 1+2,5-8)	100KHz,0.1V	4.75 μ H Max.	
	Cp	Pin(1-8) (short 2+3)	100KHz,0.1V	100pF Max.	
	DCR	Pin(4-3)	25 $\pm$ 5 $^{\circ}$ C	1040m Ω Max.	
		Pin(8-5)		75m Ω Max.	
		Pin(1-2)		1.5 Ω Max.	
	Isat	Pin(4-3)	320KHz,0.1V @L $\geq$ 90%	0.35A Min.	
	Turns Raito	Pri: Sec: Bias	15.75KHz,1V	1:0.196:0.725 $\pm$ 3%	
TWPEP121112B201T Marking:(S1612018)	Hi-Pot	Pri&Bias to Sec	50Hz, 3.0mA 1S	1500V	
	Hi-Pot	Winding to Core	50Hz, 3.0mA 1S	500V	
	L	Pin(3-2)	400KHz,0.4V	40 μ H $\pm$ 10%	
	LK	Pin(3-2) (short 5~8)	400KHz,0.4V	0.6 μ H Max.	
	DCR	Pin(3-2)	25 $\pm$ 5 $^{\circ}$ C	98m Ω Max.	
		Pin(5,6-7,8)		62m Ω Max.	
	Isat	Pin(3-2)	400KHz,0.4V @L $\geq$ 90%	1.3A Min.	
	Turns Raito	Pri: Sec	15.75KHz,1V	1:0.3 $\pm$ 3%	
TWPEP121112B202T Marking:(S1612019)	Hi-Pot	Pri to Sec	50Hz, 3.0mA 1S	1500V	
	Hi-Pot	Winding to Core	50Hz, 3.0mA 1S	500V	
	L	Pin(3-2)	400KHz,0.4V	40 μ H $\pm$ 10%	
	LK	Pin(3-2) (short 5~8)	400KHz,0.4V	0.6 μ H Max.	
	DCR	Pin(3-2)	25 $\pm$ 5 $^{\circ}$ C	98m Ω Max.	
		Pin(5,6-7,8)		45m Ω Max.	
	Isat	Pin(3-2)	400KHz,0.4V @L $\geq$ 90%	1.3A Min.	
	Turns Raito	Pri:Sec	15.75KHz,1V	1:0.4 $\pm$ 3%	
TWPEP121112B203T Marking:(S1612020)	Hi-Pot	Pri to Sec	50Hz, 3.0mA 1S	1500V	
	Hi-Pot	Winding to Core	50Hz, 3.0mA 1S	500V	
	L	Pin(3-2)	400KHz,0.4V	40 μ H $\pm$ 10%	
	LK	Pin(3-2) (short 5~8)	400KHz,0.4V	0.6 μ H Max.	
	DCR	Pin(3-2)	25 $\pm$ 5 $^{\circ}$ C	98m Ω Max.	
		Pin(5,6-7,8)		156m Ω Max.	
	Isat	Pin(3-2)	400KHz,0.4V @L $\geq$ 90%	1.3A Min.	
	Turns Raito	Pri: Sec	15.75KHz,1V	1:1 $\pm$ 3%	

SPECIFICATION

Part No.	Test Item	Test Terminal	Test Condition	Specification	Schematic
TWPEP121112B303T Marking:(S1607009)	L	Pin(3-4)	250KHz, 0.1V	155 μ H $\pm$ 10%	
	LK	Pin(3-4) (short 1+2,5~8)	250KHz, 0.1V	3.25 μ H Max.	
	DCR	Pin(3-4)	25 $\pm$ 5 $^{\circ}$ C	414m Ω Max.	
		Pin(5,6-7,8)		343m Ω Max.	
		Pin(2-1)		844m Ω Max.	
	Isat	Pin(3-4)	250KHz, 0.1V @L $\geq$ 90%	0.65A Min.	
	Turns Raito	Pri: Sec: Bias	15.75KHz,1V	1:0.667:0.667 $\pm$ 3%	
TWPEP121112B304T Marking:(S1608011)	L	Pin(3-4)	250KHz, 0.1V	155 μ H $\pm$ 10%	
	LK	Pin(3-4) (short 1+2,5~8)	250KHz, 0.1V	3.6 μ H Max.	
	DCR	Pin(3-4)	25 $\pm$ 5 $^{\circ}$ C	414m Ω Max.	
		Pin(5,6-7,8)		60m Ω Max.	
		Pin(2-1)		822m Ω Max.	
	Isat	Pin(3-4)	250KHz, 0.1V @L $\geq$ 90%	0.65A Min.	
	Turns Raito	Pri: Sec: Bias	15.75KHz,1V	1:0.262:0.667 $\pm$ 3%	
TWPEP121112B305T Marking:(S1612021)	L	Pin(3-4)	250KHz, 0.1V	155 μ H $\pm$ 10%	
	LK	Pin(3-4) (short 1+2,5~8)	250KHz, 0.1V	4.0 μ H Max.	
	DCR	Pin(3-4)	25 $\pm$ 5 $^{\circ}$ C	414m Ω Max.	
		Pin(5,6-7,8)		36m Ω Max.	
		Pin(2-1)		822m Ω Max.	
	Isat	Pin(3-4)	250KHz, 0.1V @L $\geq$ 90%	0.65A Min.	
	Turns Raito	Pri: Sec: Bias	15.75KHz,1V	1:0.19:0.667 $\pm$ 3%	
TWPEP131313B204T Marking:(S1612022)	L	Pin(2-1)	200KHz, 0.2V	40 μ H $\pm$ 10%	
	LK	Pin(2-1) (short 7~10)	250KHz, 0.1V	0.66 μ H Max.	
	DCR	Pin(2-1)	25 $\pm$ 5 $^{\circ}$ C	100m Ω Max.	
		Pin(7,8-9,10)		25m Ω Max.	
	Isat	Pin(2-1)	200KHz, 0.2V @L $\geq$ 90%	2.0A Min.	
	Turns Raito	Pri:Sec	15.75KHz,1V	1:0.3 $\pm$ 3%	
	Hi-Pot	Pri to Sec	50Hz, 3.0mA 1S	1500V	
		Winding to Core	50Hz, 3.0mA 1S	500V	

SPECIFICATION

Part No.	Test Item	Test Terminal	Test Condition	Specification	Schematic
TWPEP131313B205T Marking:(S1612023)	L	Pin(2-1)	200KHz, 0.2V	40 μ H $\pm$ 10%	
	LK	Pin(2-1) (short 7~10)	250KHz, 0.1V	0.621 μ H Max.	
	DCR	Pin(2-1)	25 $\pm$ 5 $^{\circ}$ C	100m Ω Max.	
		Pin(7,8-9,10)		40m Ω Max.	
	Isat	Pin(2-1)	200KHz, 0.2V @L $\geq$ 90%	2.0A Min.	
	Turns Raito	Pri: Sec	15.75KHz,1V	1:0.4 $\pm$ 3%	
TWPEP131313B206T Marking:(S1612024)	L	Pin(2-1)	200KHz, 0.2V	40 μ H $\pm$ 10%	
	LK	Pin(2-1) (short 7~10)	250KHz, 0.1V	0.566 μ H Max.	
	DCR	Pin(2-1)	25 $\pm$ 5 $^{\circ}$ C	100m Ω Max.	
		Pin(7,8-9,10)		155m Ω Max.	
	Isat	Pin(2-1)	200KHz, 0.2V @L $\geq$ 90%	2.0A Min.	
	Turns Raito	Pri: Sec	15.75KHz,1V	1:1 $\pm$ 3%	
TWPEP131313B301T Marking:(S1612025)	L	Pin(3-4)	250KHz, 0.1V	127 μ H $\pm$ 10%	
	LK	Pin(3-4) (short 1+2,7~10)	250KHz, 0.1V	1.25 μ H Max.	
	DCR	Pin(3-4)	25 $\pm$ 5 $^{\circ}$ C	255m Ω Max.	
		Pin(7,8-9,10)		24m Ω Max.	
		Pin(2-1)		310m Ω Max.	
	Isat	Pin(3-4)	250KHz, 0.1V @L $\geq$ 90%	1.0A Min.	
TWPEP131313B303T Marking:(S1608025)	L	Pin(3-5)	200KHz, 0.1V	37 μ H $\pm$ 10%	
	LK	Pin(3-5) (short 1+2,6~10)	200KHz, 0.1V	1.0 μ H Max.	
	DCR	Pin(3-5)	25 $\pm$ 5 $^{\circ}$ C	89m Ω Max.	
		Pin(6,7-9,10)		7.5m Ω Max.	
		Pin(2-1)		84m Ω Max.	
	Isat	Pin(3-4)	200KHz, 0.1V @L $\geq$ 90%	2.0A Min.	

SPECIFICATION

Part No.	Test Item	Test Terminal	Test Condition	Specification	Schematic
TWPEP131313B304T Marking:(S1612026)	L	Pin(3-4)	250KHz, 0.1V	127 μ H $\pm$ 10%	
	LK	Pin(3-4) (short 1+2,7~10)	250KHz, 0.1V	0.95 μ H Max.	
	DCR	Pin(3-4)	25 $\pm$ 5 $^{\circ}$ C	222m Ω Max.	
		Pin(7,8-9,10)		39m Ω Max.	
		Pin(2-1)		348m Ω Max.	
	Isat	Pin(3-4)	250KHz, 0.1V @L $\geq$ 90%	1.0A Min.	
	Turns Raito	Pri: Sec: Bias	15.75KHz,1V	1:0.25:0.5 $\pm$ 3%	
TWPEP131313B305T Marking:(S1612027)	L	Pin(3-4)	250KHz, 0.1V	127 μ H $\pm$ 10%	
	LK	Pin(3-4) (short 1+2,7~10)	250KHz, 0.1V	0.95 μ H Max.	
	DCR	Pin(3-4)	25 $\pm$ 5 $^{\circ}$ C	199m Ω Max.	
		Pin(7,8-9,10)		65m Ω Max.	
		Pin(2-1)		348m Ω Max.	
	Isat	Pin(3-4)	250KHz, 0.1V @L $\geq$ 90%	1.0A Min.	
	Turns Raito	Pri: Sec: Bias	15.75KHz,1V	1:0.5:0.5 $\pm$ 3%	
TWPEP131313B306T Marking:(S1612028)	L	Pin(3-4)	250KHz, 0.1V	127 μ H $\pm$ 10%	
	LK	Pin(3-4) (short 1+2,7~10)	250KHz, 0.1V	0.8 μ H Max.	
	DCR	Pin(3-4)	25 $\pm$ 5 $^{\circ}$ C	290m Ω Max.	
		Pin(7,8-9,10)		66m Ω Max.	
		Pin(2-1)		355m Ω Max.	
	Isat	Pin(3-4)	250KHz, 0.1V @L $\geq$ 90%	1.0A Min.	
	Turns Raito	Pri: Sec: Bias	15.75KHz,1V	1:0.567:0.5 $\pm$ 3%	
TWPEP131313B307T Marking:(S1612029)	L	Pin(3-4)	250KHz, 0.1V	127 μ H $\pm$ 10%	
	LK	Pin(3-4) (short 1+2,7~10)	250KHz, 0.1V	0.6 μ H Max.	
	DCR	Pin(3-4)	25 $\pm$ 5 $^{\circ}$ C	290m Ω Max.	
		Pin(7,8-9,10)		67m Ω Max.	
		Pin(2-1)		355m Ω Max.	
	Isat	Pin(3-4)	250KHz, 0.1V @L $\geq$ 90%	1.0A Min.	
	Turns Raito	Pri: Sec: Bias	15.75KHz,1V	1:0.667:0.5 $\pm$ 3%	
	Hi-Pot	Pri&Bias to Sec	50Hz, 3.0mA 1S	1500V	Suggest Equivalent Circuit
		Winding to Core	50Hz, 3.0mA 1S	500V	
		Winding to Core	50Hz, 3.0mA 1S	500V	

SPECIFICATION

Part No.	Test Item	Test Terminal	Test Condition	Specification	Schematic
TWPEP131313B324T Marking:(S1701001)	L	Pin(3-5)	10KHz, 0.1V	100 μ H $\pm$ 10%	Suggest Equivalent Circuit
	LK	Pin(3-5) (short 1+2,6~10)	100KHz, 0.1V	1.5 μ H Max	
	DCR	Pin(3-5)	25 $\pm$ 5 $^{\circ}$ C	185m Ω Max	
		Pin(6,7-9,10)		31m Ω Max	
		Pin(2-1)		190m Ω Max	
	Isat	Pin(3-5)	10KHz, 0.1V @L $\geq$ 90%	2.0A Min	
	Turns Raito	Pri: Sec: Bias	15.75KHz,1V	1:0.33:0.22 $\pm$ 3%	
TWPEP131313B408T Marking:(S1610013)	L	Pin(5-4)	100KHz, 0.1V	127 μ H $\pm$ 10%	Suggest Equivalent Circuit
	LK	Pin(5-4) (short 1+2,6~10)	100KHz, 0.1V	1.0 μ H Max.	
	DCR	Pin(5-4)	25 $\pm$ 5 $^{\circ}$ C	200m Ω Max.	
		Pin(10-8)		16.5m Ω Max.	
		Pin(1-2)		400m Ω Max.	
		Pin(7-6)		400m Ω Max.	
	Isat	Pin(3-5)	100KHz, 0.1V @L $\geq$ 90%	1.0A Min.	
	Turns Raito	Pri: Sec: Bias:Synch	15.75KHz,1V	1:0.167:0.611:0.25 $\pm$ 3%	
TWPEP131313B411T Marking:(S1612030)	L	Pin(3-4)	200KHz, 0.1V	480 μ H $\pm$ 10%	Suggest Equivalent Circuit
	Lk	Pin(3-4) (short 1+2,6~10)	200KHz, 0.1V	10.0 μ H Max.	
	DCR	Pin(3-4)	AT 25 $\pm$ 5 $^{\circ}$ C	570m Ω Max.	
		Pin(8-10)		15m Ω Max.	
		Pin(6-7)		40m Ω Max.	
		Pin(2-1)		325m Ω Max.	
	Isat	Pin(3-5)	200KHz, 0.1V @L $\geq$ 90%	0.5A Min.	
TWPEE203113B403T Marking:(S2009007)	L	Pin(1,2,3,4)	100kHz,0.1V	23.8 μ H $\pm$ 10%	Suggest Equivalent Circuit
	Lk	Pin(1,2-3,4) (short other Pins)	100KHz, 0.1V	0.56 μ H Max.	
	DCR	Pin(1,2-3,4)	AT 25 $\pm$ 5 $^{\circ}$ C	82m Ω Max.	
		Pin(12,11,10-7,8,9)		18m Ω Max.	
		Pin(5,6)		143 Max.	
	Turns Raito	(1,2-3,4):(5,6) : (12,11,10-7,8,9)	15.75KHz,1V	16:6:6	
	Hi-Pot	Pri to Sec	1500VAC/50Hz/1mA/3sec	1500V	

Safety Transformer

FEATURES

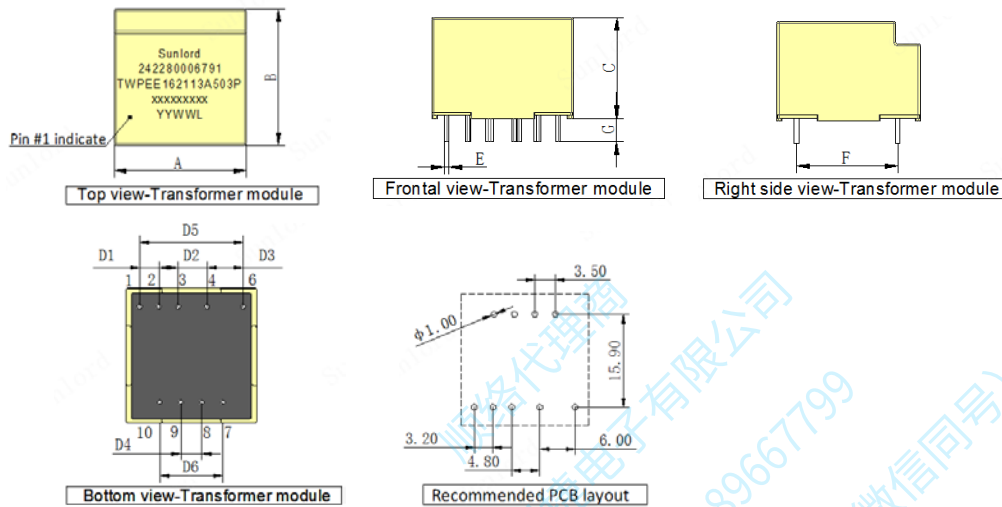
- Pouring glue structure, good moisture-proof, water-proof and dust-proof performance
- Low loss, low leakage inductance, uniform magnetic field, high EMC Performance
- Compliant IEC60335-1&IEC61558-2-16 Safety Certificate

APPLICATIONS

- Widely used in AC/DC switches power supply

SHAPE AND DIMENSIONS

■ P/N: TWPEE162113A503P



Unit: mm

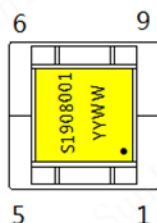
ITEM	A	B	C	D1	D2	D3
LIMIT	21.90±0.30	22.60±0.30	16.0 Max.	3.20±0.20	4.80±0.20	6.00±0.20
ITEM	D4	D5	D6	E	F	G
LIMIT	3.50±0.20	17.20±0.20	10.50±0.20	0.70±0.10	15.90±0.20	3.50±0.30

SPECIFICATION

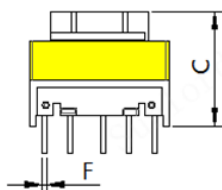
Part No.	Test Item	Test Terminal	Test Condition	Specification	Schematic
TWPEE162113A503P	L	Pin(4-6)	100kHz,0.1V	5.0mH±10%	
	Lk	Pin(4-6) (short 1,2,3,8,9)	100kHz,0.1V	50μH Max.	
	DCR	Pin(4-6)	25±5℃	6.0Ω Max.	
		Pin(3-1)		1.2Ω Max.	
		Pin(8-9)		0.3Ω Max.	
	Turns Raito	Pin(4-6): (3-2): (2-1): (8-9)	100kHz,0.1V	120:12:14:15 Ts	
	Hi-PoT	Pri , Aux to Sec	3400Vac/50Hz/ 0.5mA/60Sec	No breakdown	

SHAPE AND DIMENSIONS

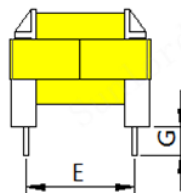
■ P/N: TWPEE212118A500P



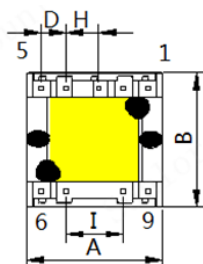
Top View



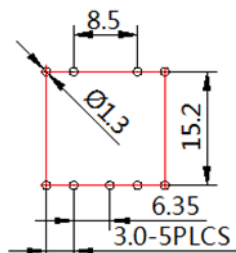
Right Side View



Frontal View



Bottom View



Recommended PCB Layout

Unit: mm

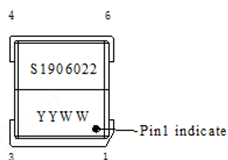
ITEM	A	B	C	D	E	F	G	H	I
LIMIT	21.0 Max.	21.0 Max.	18.0 Max.	3.0±0.3	15.2±0.5	SQ0.64±0.10	4.0±0.5	6.35±0.3	8.5±0.3

SPECIFICATION

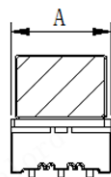
Part No.	Test Item	Test Terminal	Test Condition	Specification	Schematic
TWPEE212118A500P	L	Pin(1-3)	100kHz,0.1V	1.4mH ±10%	
	Lk	Pin(1-3), shorted other pins	100kHz,0.1V	70.0uH Max.	
	DCR	Pin(1-3)	25±5℃	15.0 Ω Max.	
		Pin(4-5)		5.0 Ω Max.	
		Pin(7-6)		0.7 Ω Max.	
		Pin(9-8)		0.6 Ω Max.	
	Turns Ratio	Pin(1-3):(4-5):(7-6):(9-8)	15.75KHz, 1.0V	148:49:49:49Ts	
	Hi-PoT	N1&N5 to N2, N1&N5 to N3, N2 to N3	3000Vac/50Hz/1 mA/ 60 Sec	No breakdown	

SHAPE AND DIMENSIONS

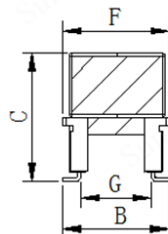
P/N: TWPEP131517B400T



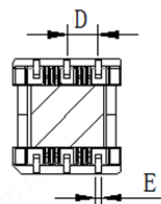
Top View



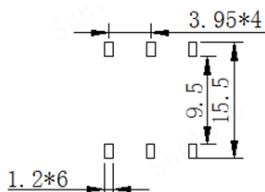
Right Side View



Frontal View



Bottom View



Recommended PCB Layout

Unit: mm

ITEM	A	B	C	D	E	F	G	Co-planarity
LIMIT	16.0 Max	16.5 Max	17.0 Max	3.95±0.15	0.80±0.1	14.6±0.5	9.60TY P	0.15 Max.

SPECIFICATION

Part No.	Test Item	Test Terminal	Test Condition	Specification	Schematic
TWPEE212118A500P	Lpri	Pin(1-3)	100KHz ,20mV	142uH ±20%	
	Lsec	Pin(6-4)	100KHz ,20mV	500~610 uH	
	Lk	Pin(1-3), shorted other pins	100KHz ,0.25V	4.0 uH Max.	
	DCR	Pin(1-3)	25±5℃	0.50 Ω Max.	
	Cp	Pin (1-6)	100KHz ,20mV	15.0 Max.	
	IpK	Pin(1-3)	100KHz ,20mV, 20% Rolloff from initial	0.4 A Min.	
	Hi-PoT	N1&N5 to N2, N1&N5 to N3, N2 to N3	3000Vac/50Hz/1mA/ 60 Sec	No breakdown	
	Turns Ratio	Pin(1-3):(4-5):(7-6):(9-8)	15.75KHz, 1.0V	148:49:49:49Ts	